CLIPPEDIMAGE= JP403286518A

PAT-NO: JP403286518A

DOCUMENT-IDENTIFIER: JP 03286518 A

TITLE: MANUFACTURE OF THIN SEMICONDUCTOR CRYSTAL LAYER

PUBN-DATE: December 17, 1991

INVENTOR-INFORMATION:

NAME

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COUNTRY

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APPL-NO: JP02087977

APPL-DATE: April 2, 1990

INT-CL (IPC): H01L021/20; H01L021/263 ; G02F001/136

US-CL-CURRENT: 438/949,438/FOR.439

ABSTRACT:

PURPOSE: To laminate a thin semiconductor crystal layer which is uniform and whose quality is good and to be provided with a characteristic as an element formation substrate for a three-dimensional semiconductor device by a method wherein the shape of a beam is deformed to be a sheet shape, the beam is scanned and, at the same time, a thin semiconductor film is irradiated with the beam.

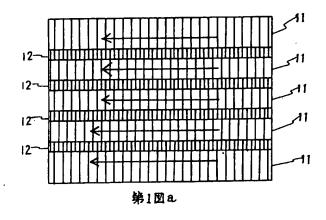
CONSTITUTION: A silicon layer 32 is formed on the whole surface of a square glass substrate 31. When a laser beam is scanned, it is moved at a speed of 1[mm/s] in the Y-direction. The width of the laser beam in the X-direction is

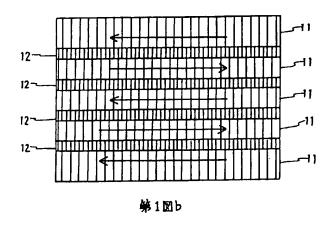
adjusted by changing the distance between a concave lens 22 and a convex lens
23. The energy density of the laser beam is adjusted by the distance between a speciemen and a convex lens 25. When the distance is to be shortened, a convex lens whose curvature is large is used. Thereby, the scanning direction of the laser beam is only in the Y-direction. Thereby, it is possible to prevent the annealing operation of the silicon layer from being overlapped and to execute the annealing operation in order to obtain the silicon layer whose physical properties are good and whose quality is good.

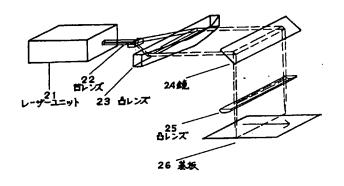
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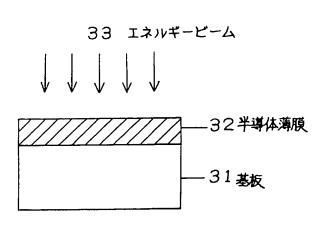
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第 2 図



第 3 図

